

SRC1212N

NPN Silicon Transistor

Descriptions

- Switching application
- Interface circuit and driver circuit application

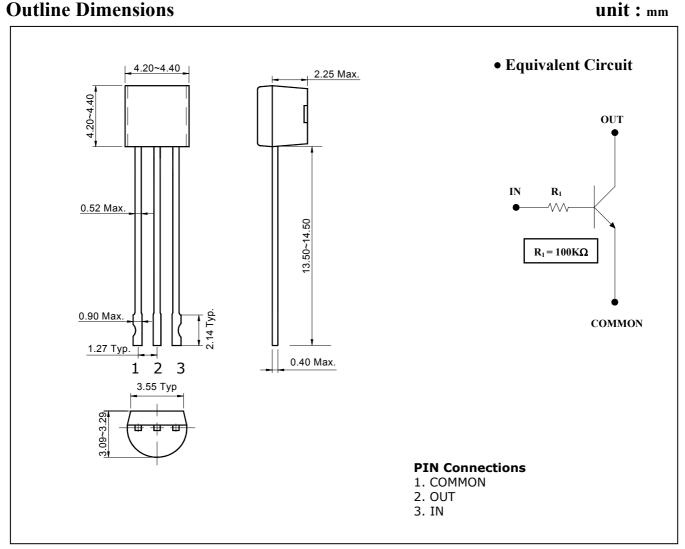
Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary pair with SRA2212N

Ordering Information

Type NO.	Marking	Package Code		
SRC1212N	SRC1212	TO-92N		

Outline Dimensions



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Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	50	V
Input voltage	$V_{\rm I}$	40, -5	V
Output current	I_{O}	100	mA
Power dissipation	P_D	400	mW
Junction temperature	T _J	150	°C
Storage temperature range	T_{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V _O =50V, V _I =0	-	-	500	nA
DC current gain	G_{I}	V _O =5V, I _O =10mA	120	-	-	ı
Output voltage	$V_{O(ON)}$	I_O =10mA, I_I =0.5mA	-	0.1	0.3	٧
Input voltage (ON)	$V_{I(ON)}$	V _O =0.2V, I _O =5mA	-	-	4.4	V
Input voltage (OFF)	$V_{I(OFF)}$	V _O =5V, I _O =0.1mA	0.3	-	1	V
Transition frequency	f _T *	$V_0=10V$, $I_0=5mA$, $f=1MHz$	-	200	1	MHz
Input current	$I_{\rm I}$	$V_I=5V$, $I_O=0$	-	-	0.1	mA
Input resistor (Input to base)	R ₁	-	70	100	130	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 I_0 - $V_{I(ON)}$

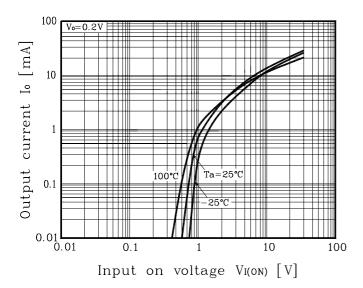


Fig. 2 I_O - $V_{I(OFF)}$

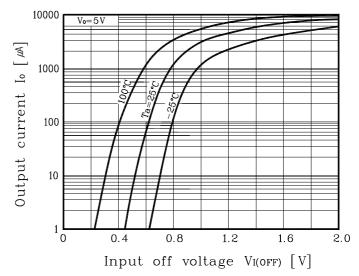
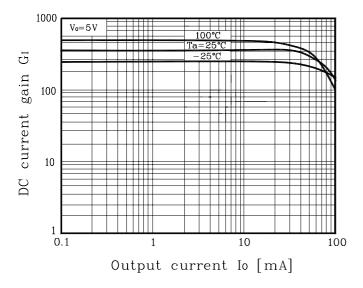


Fig. 3 G_I - I_O



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